SUPERMICR • X13SCL-IF QUICK REFERENCE GUIDE 1.00

Motherboard Layout and Features

CONTACT INFORMATION

- Website: www.supermicro.com
- General Information: marketing@supermicro.com
 Technical Support: support@supermicro.com
 Phone: +1 (408) 503-8000, Fax: +1 (408) 503-8008

FOR YOUR SYSTEM TO WORK PROPERLY, PLEASE DOWNLOAD APPROPRIATE DRIVERS/IMAGES/USER'S MANUAL FROM THE LINKS BELOW:

• Manuals: http://www.supermicro.com/support/manuals

Jumpers and Connectors

Drivers & Utilities: https://www.supermicro.com/wdl/driver/

Safety: http://www.supermicro.com/about/policies/safety_information.cfm

PACKAGE **C**ONTENTS

- One Quick Reference Guide • One I/O Shield
- Two SATA Cables

BMC Contr D3619 JMD1_SRW1 F@ ((**SUPER** X13SCL JSB4/5 (3.2(5Gb BAR CODE -SATA4 I-SATA C262 ō Cle 4.0 x4 / SATA v. = mounting hole

Note: Graphics shown in this guick reference guide are for illustration only. Your components may or may not look exactly the same as drawings shown in this guide.



Back Panel I/O Connectors						
	2					
5	3			9 10		
	#	4 6 8 Description	#	Description		
	1.	COM1	6.	USB2 (USB 3.2 Gen 1)		
	2.	BMC LAN Port	7.	LAN2		
	3.	USB1 (USB 2.0)	8.	LAN1		
	4.	USB0 (USB 2.0)	9.	VGA		
	5.	USB3 (USB 3.2 Gen 1)	10.	UID Switch		

Jumpers and v	Sonnectors			
	Jumpers			
Jumper	Description	Default Setting	LED	
JBT1	CMOS Clear	Open (Normal)		
JNS1	SATA 3.0 or PCIe 4.0	Pins 1–2 (4x SATA)		
	Selection	Pins 2–3 (PCIe x4)		
JPG1	VGA Enable/Disable	Pins 1–2 (Enabled)		
JPL1	LAN1 Enable/Disable	Pins 1–2 (Enabled)	LEL	
JPL2	LAN2 Enable/Disable	Pins 1–2 (Enabled)		
JPME2	Manufacturing Mode	Pins 1–2 (Normal)	C	
JRF1	CPU PCIe Bifurcation	Pins 1–2 (JPCIE x16)		
JWD1	Watch Dog Timer	Pins 1–2 (Reset)		
			Th Pe	
	Connectors			
Connector	Description			
BMC_LAN	Dedicated BMC LAN Port			
BT1	Onboard Battery		M	
COM1	COM Port			
FAN1-FAN3, FANA	CPU/System Fan Headers (FAN1: CPU Fan)		Tł	
I-SATA4, I-SATA5	Intel® PCH SATA 3.0 Ports		slo m	
JD1	Speaker			
JF1	Front Control Panel Heade	r		
JL1	Chassis Intrusion Header			
JPI ² C1	Power I ² C System Management Bus (Power SMB) Header		l v	
JPW1	24-pin ATX Power Connec	tor (Required)	f	
JPW2	4-pin Power Connector			
JSMB1	System Management Bus Header			
JTPM1	Trusted Platform Module (TPM)/Port 80 Connector			
JUIDB1	UID Switch			
LAN1/2	Gigabit Ethernet RJ45 Ports			
M.2-C	M.2 PCle 4.0 x4 Slot (supports 2280 form factor)			
MH1–MH4	Mounting Holes	, , , , , , , , , , , , , , , , , , ,		
SLOT7	PCIe 5.0 x16 Slot			
USB0/1	Back Panel USB 2.0 Ports			
USB2/3	Back Panel USB 3.2 Gen	1 Ports		
USB4/5	Front Panel USB 3.2 Gen			

Note: Refer to Chapter 2 of the User Manual for detailed information on jumpers, connectors, and LED indicators.

VGA Port

VGA

Nun



WARNING: This product can expose you to chemicals includin lead, known to the State of California to cause cancer and birth defects or other reproductive harm. For more information, go to www.P65Warnings.ca.gov.

LED Indicators

LED Indicators				
Description	Status			
Overheat/Power Fail/Fan Fail LED	Solid Red: System Overheat Blinking Red: Power Fail or Fan Fail			
UID LED	Solid Blue: Unit Identified			
Onboard Power LED	Solid Green: Power On			
BMC Heartbeat LED	Blinking Green: Device Working			

U Support

X13SCL-IF motherboard supports an Intel Xeon E-2400 or 12th Generation tium processor with up to eight cores in a (V0 - LGA1700 socket.

mory Support

X13SCL-IF motherboard supports up to 64 GB of DDR5 ECC UDIMM in two S. Speed support is up to 4800 MT/s. For optimal performance, use memory ules of the same type and speed.

DIMM Memory Installation

nen installing memory modules, the DIMM slots should be populated in the lowing order: DIMMA1, DIMMB1.

It is recommended to use DIMM modules of the same type, size, and speed. Mixed DIMM speeds can be installed. However, all DIMMs will run at the speed of the slowest DIMM.

The motherboard will support odd-numbered modules. However, for best memory performance, install DIMM modules in pairs to activate memory interleaving.

1 CPU, 4 DIMM Slots				
nber of DIMMs	Memory Population Sequence			
1	DIMMB1 DIMMA1			
2	DIMMB1 / DIMMA1			